



# CAKEML

A Verified Implementation of ML

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# Dimensions of Compiler Verification

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Source Code

Abstract Syntax

Intermediate Language

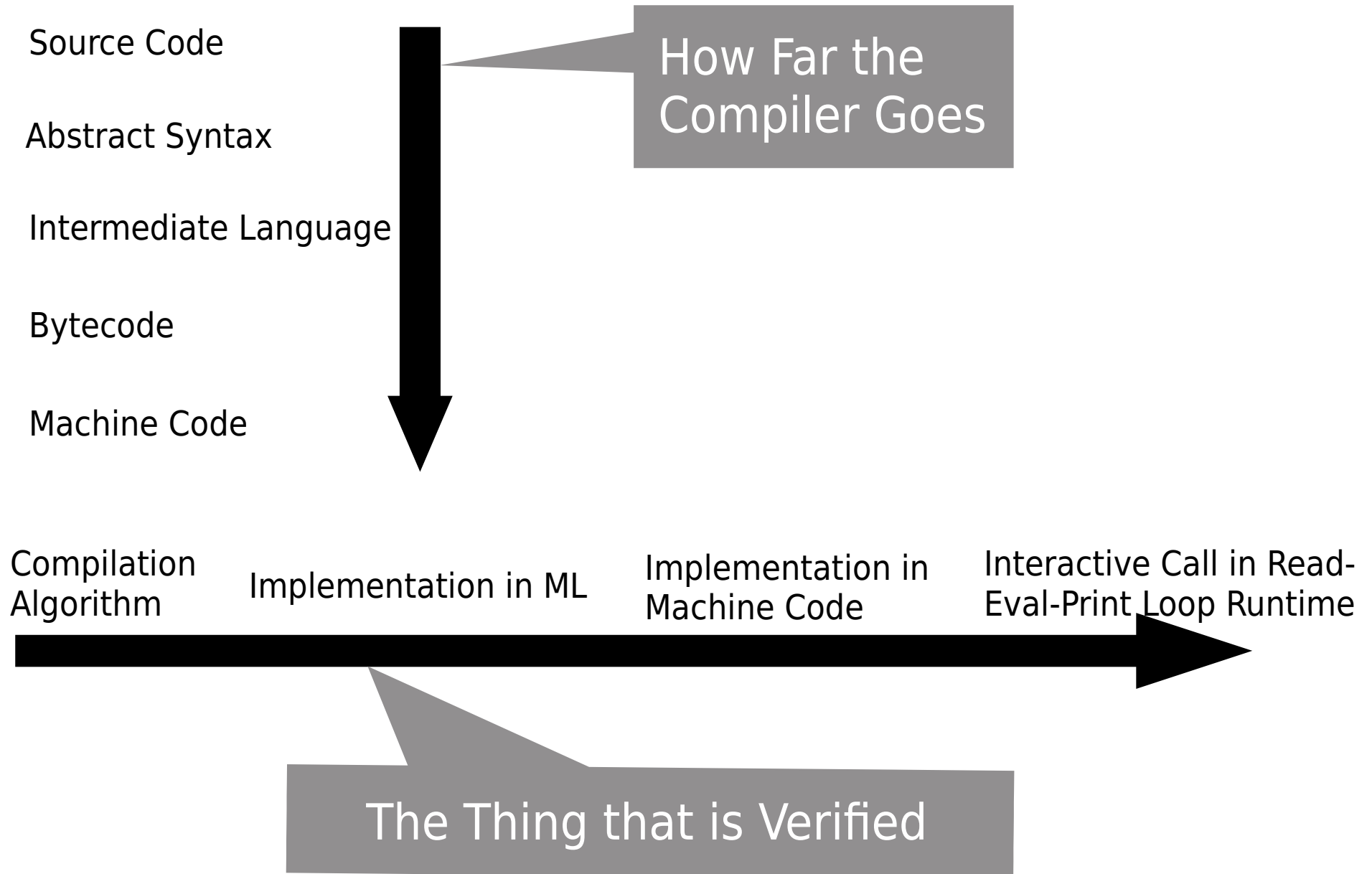
Bytecode

Machine Code

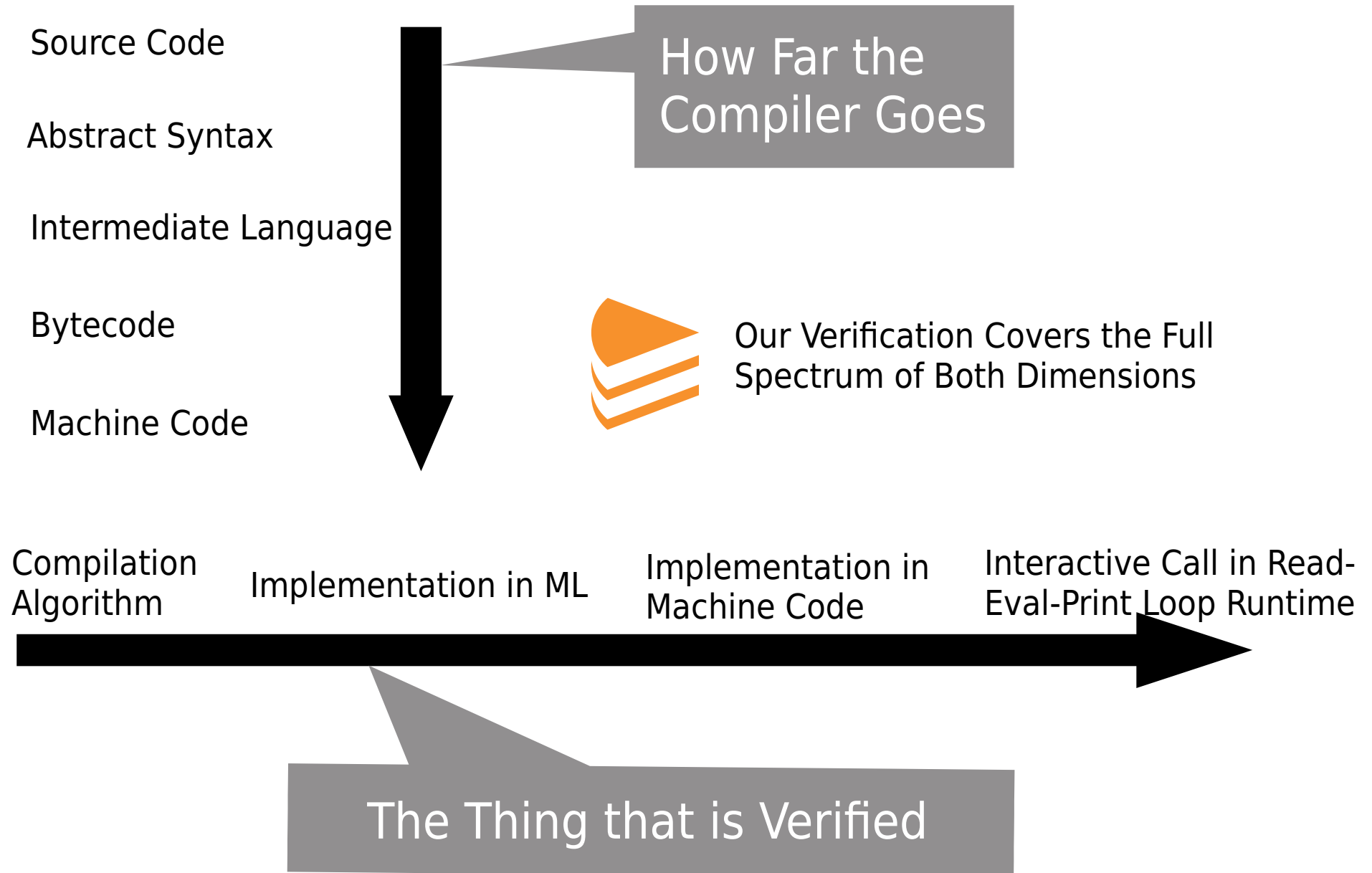


How Far the  
Compiler Goes

# Dimensions of Compiler Verification



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# The Size of the Language

CakeML, the language

= Standard ML without I/O or functors

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CakeML, the language

= Standard ML without I/O or functors

i.e., most everything else:

- ✓ higher-order functions,
- ✓ mutual recursion and polymorphism,
- ✓ datatypes and pattern matching,
- ✓ references and exceptions,
- ✓ modules and signatures

# Contributions

Artefacts

Proof Techniques



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## Artefacts

Specifications

Verified Algorithms

Verified Executable

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## Proof Techniques

Interactive Proof

x86 Synthesis

ML Synthesis

Bootstrapping

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including new inductive  
approach to **divergence**  
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## Proof Techniques

Interactive Proof

x86 Synthesis

ML Synthesis

Bootstrapping

main new technique: use verified compiler to produce verified implementation

# Approach

Proof by refinement:

**Step 1: specification** of CakeML language

- big-step and small-step operational semantics

**Step 2: functional implementation** in logic

- read-eval-print loop as a verified function

**Step 3: production of verified x86-64 code**

- produced mainly by bootstrapping the compiler

# Operational Semantics

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## Big-step evaluation relation

environment semantics, **closures**

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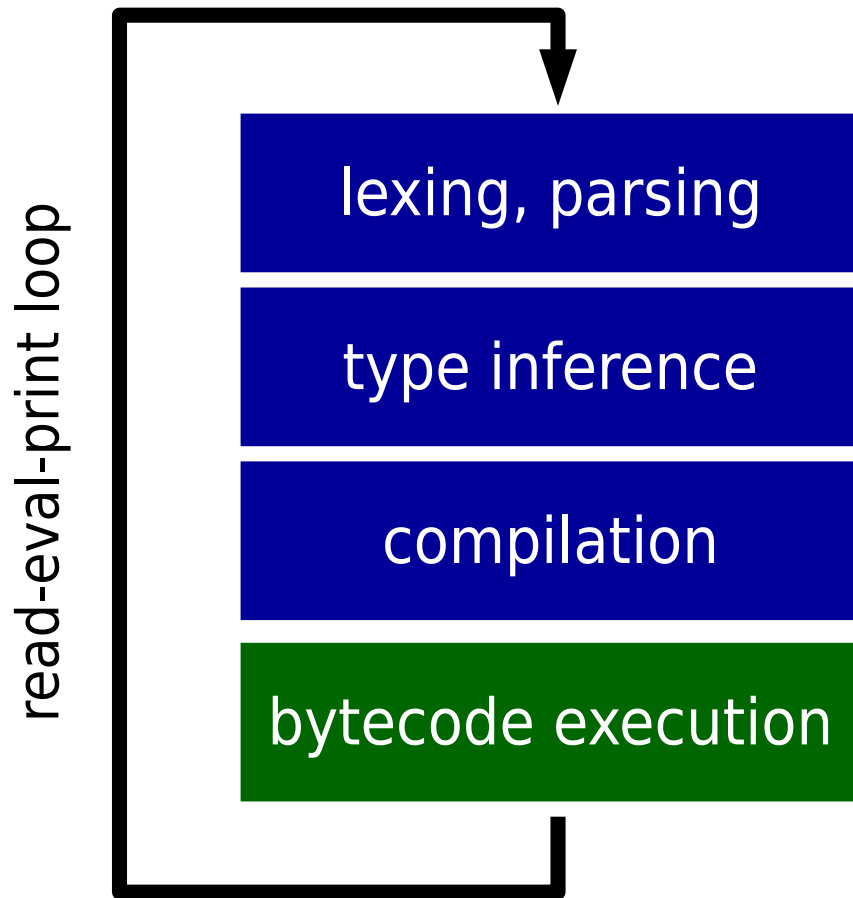
## Small-step CEK machine

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## Read-eval-print loop semantics

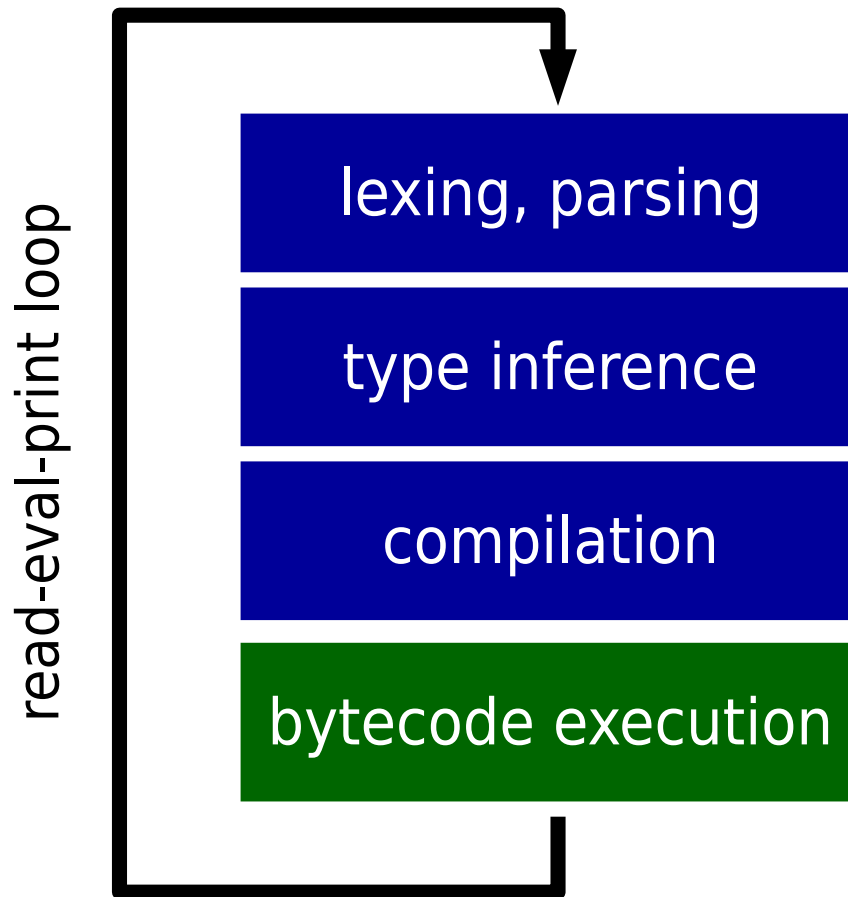
relates **input string** to list of **output strings**  
ending in termination or divergence

# Functional Implementation



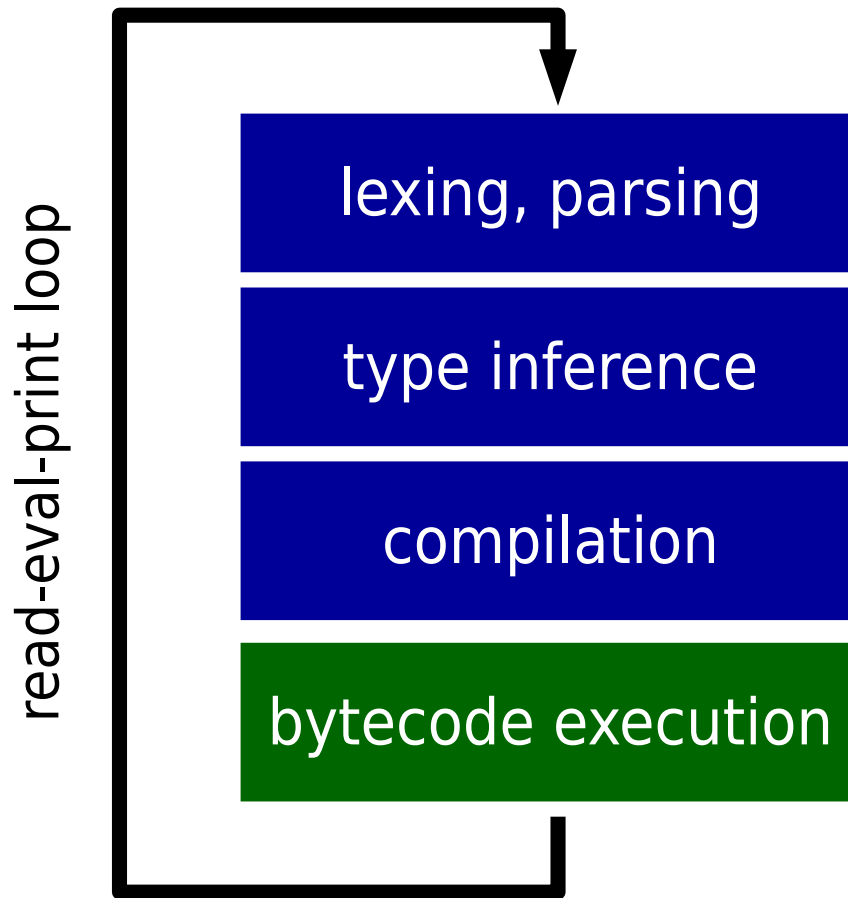
# Functional Implementation

Defined as a function in the logic of HOL4



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and verified to  
implement the  
read-eval-print  
loop semantics

# lexing, parsing

## Specification

Context-Free Grammar (CFG) for significant subset of SML

Executable Lexer

## Implementation

Parsing-Expression-Grammar (PEG) Parser

inductive evaluation relation

executable interpreter for PEGs

## Correctness

Soundness and Completeness

induction on length of token list/parse tree and non-terminal rank

# type inference

## Specification

Declarative type system

## Implementation

Based on Algorithm J

Purely functional (state-exception monad)

## Correctness

Proved soundness: every inferred type is derivable

Re-used existing work on verified unification [ITP 2010]

# compilation

## Purpose

Translate type-checked CakeML to CakeML Bytecode

## Implementation

Translation via an intermediate language (IL).

Big-step operational semantics.

CakeML to IL: make language more uniform, de Bruijn indices

IL to IL: remove pattern matching, free-variable analysis

IL to Bytecode: closure conversion, data refinement, tail-call opt.

# Semantics of bytecode execution

## Instructions:

$bc\_inst$  ::= Stack  $bc\_stack\_op$  | PushExc | PopExc  
| Return | CallPtr | Call  $loc$   
| PushPtr  $loc$  | Jump  $loc$  | JumpIf  $loc$   
| Ref | Deref | Update | Print | PrintC char  
| Label  $n$  | Tick | Stop

$bc\_stack\_op$  ::= Pop | Pops  $n$  | Shift  $n$   $n$  | PushInt int  
| Cons  $n$   $n$  | El  $n$  | TagEq  $n$  | IsBlock  $n$   
| Load  $n$  | Store  $n$  | LoadRev  $n$   
| Equal | Less | Add | Sub | Mult | Div | Mod

## Small-step semantics. Values and States:

$bc\_value$  ::= Number int | RefPtr  $n$  | Block  $n$   $bc\_value^*$   
| CodePtr  $n$  | StackPtr  $n$

$bc\_state$  ::= { stack :  $bc\_value^*$ ; refs :  $n \mapsto bc\_value$ ;  
code :  $bc\_inst^*$ ; pc :  $n$ ; handler :  $n$ ;  
output : string; names :  $n \mapsto$  string;  
clock :  $n^?$  }



# Semantics of bytecode execution

Sample rules:

$$\frac{\text{fetch}(bs) = \text{Stack} (\text{Cons } t \ n) \quad bs.\text{stack} = vs @ xs \quad |vs| = n}{bs \rightarrow (\text{bump } bs)\{\text{stack} = \text{Block } t \ (\text{rev } vs) :: xs\}}$$

$$\frac{\text{fetch}(bs) = \text{Return} \quad bs.\text{stack} = x :: \text{CodePtr } ptr :: xs}{bs \rightarrow bs\{\text{stack} = x :: xs; \text{pc} = ptr\}}$$

$$\frac{\text{fetch}(bs) = \text{CallPtr} \quad bs.\text{stack} = x :: \text{CodePtr } ptr :: xs}{bs \rightarrow bs\{\text{stack} = x :: \text{CodePtr } (\text{bump } bs).\text{pc} :: xs; \text{pc} = ptr\}}$$

# compilation

## Correctness

Proved in the **direction of compilation**.

**Data refinement** for a closure uses label from closure conversion. Inductive proof assumes this points to the compilation of the body.

Shape of correctness theorem:

$$env \vdash exp \Downarrow val \implies$$

“the code for *exp* is installed in *bs* etc.”  $\implies$

$$\exists bs'. bs \rightarrow^* bs' \wedge \text{“}bs' \text{ contains } val\text{”}$$

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IL big-step eval

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bytecode next step reln.

What about divergence?

# Idea: add logical clock

Big-step semantics:

- has an optional **clock** component
- **clock 'ticks'** decrement every function application
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for every clock env exp

there is a result

either: Result  
or TimeOut

$\forall \text{clock env exp}. \exists \text{res}. (\text{Some clock, env}) \vdash \text{exp} \Downarrow \text{res}$

produced by the semantics



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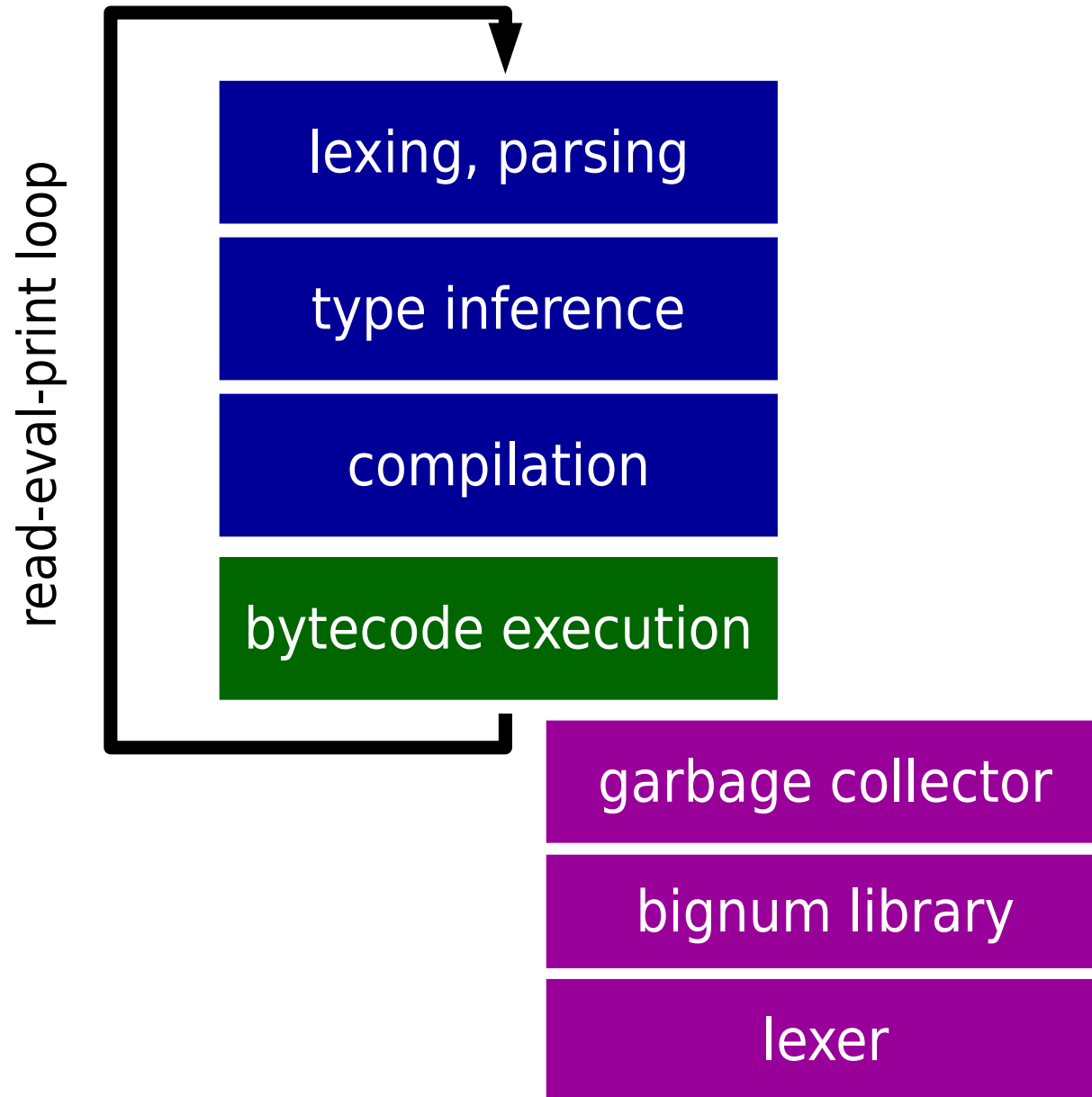
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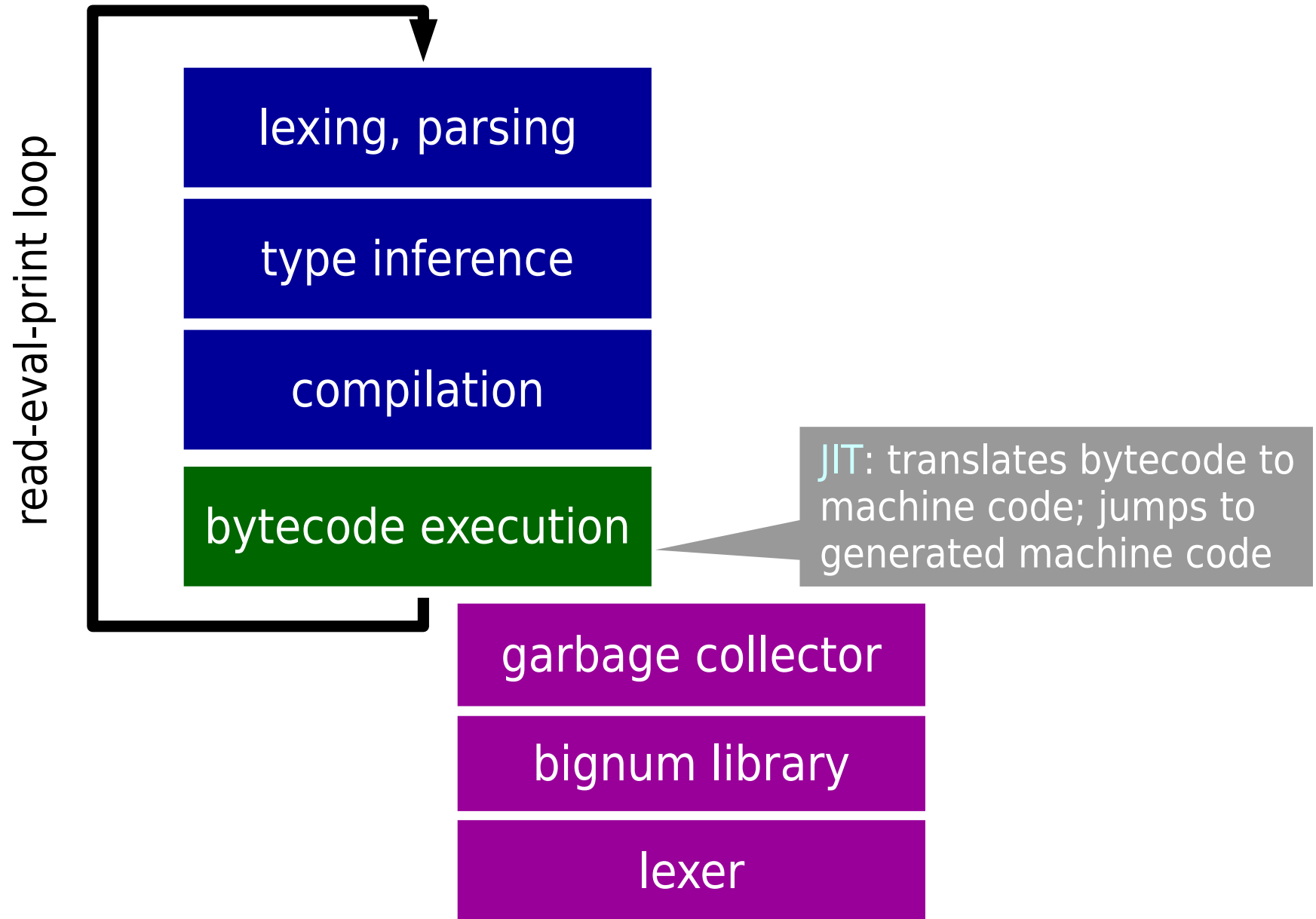
Theorem: bytecode diverges if and only if CakeML evaluation diverges

Step 3: production of **verified x86-64 code**

# Verified Implementation in x86-64

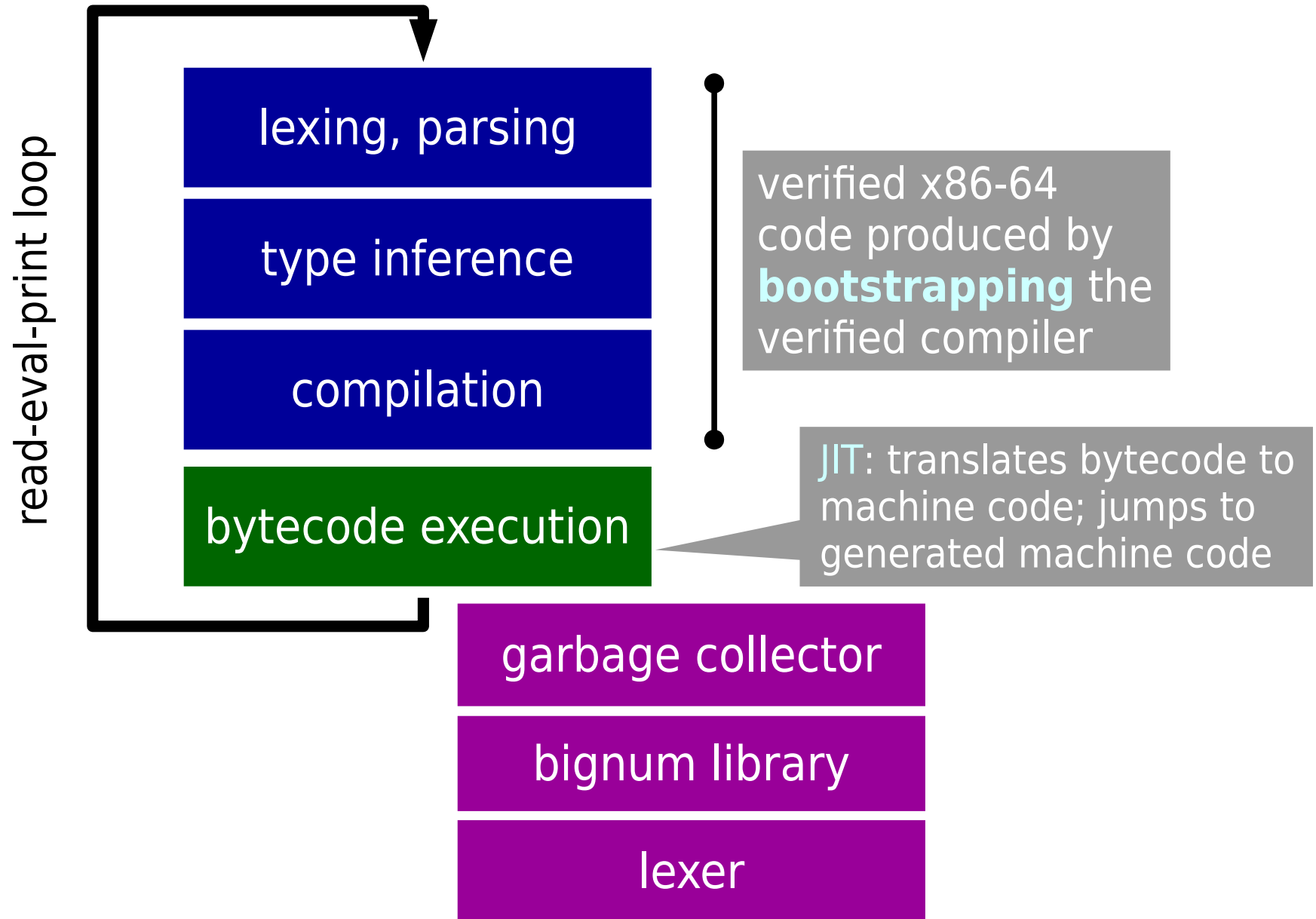


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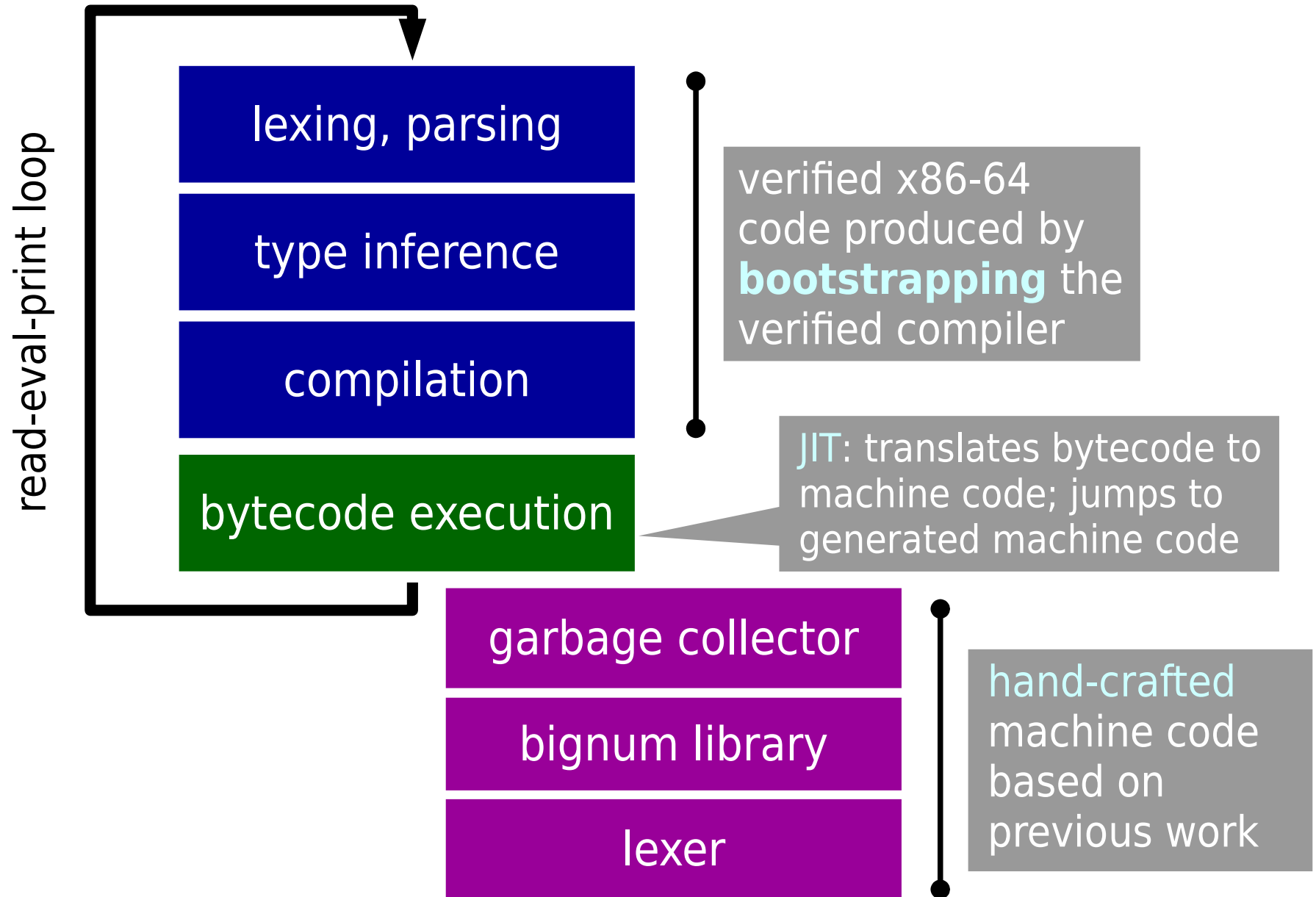




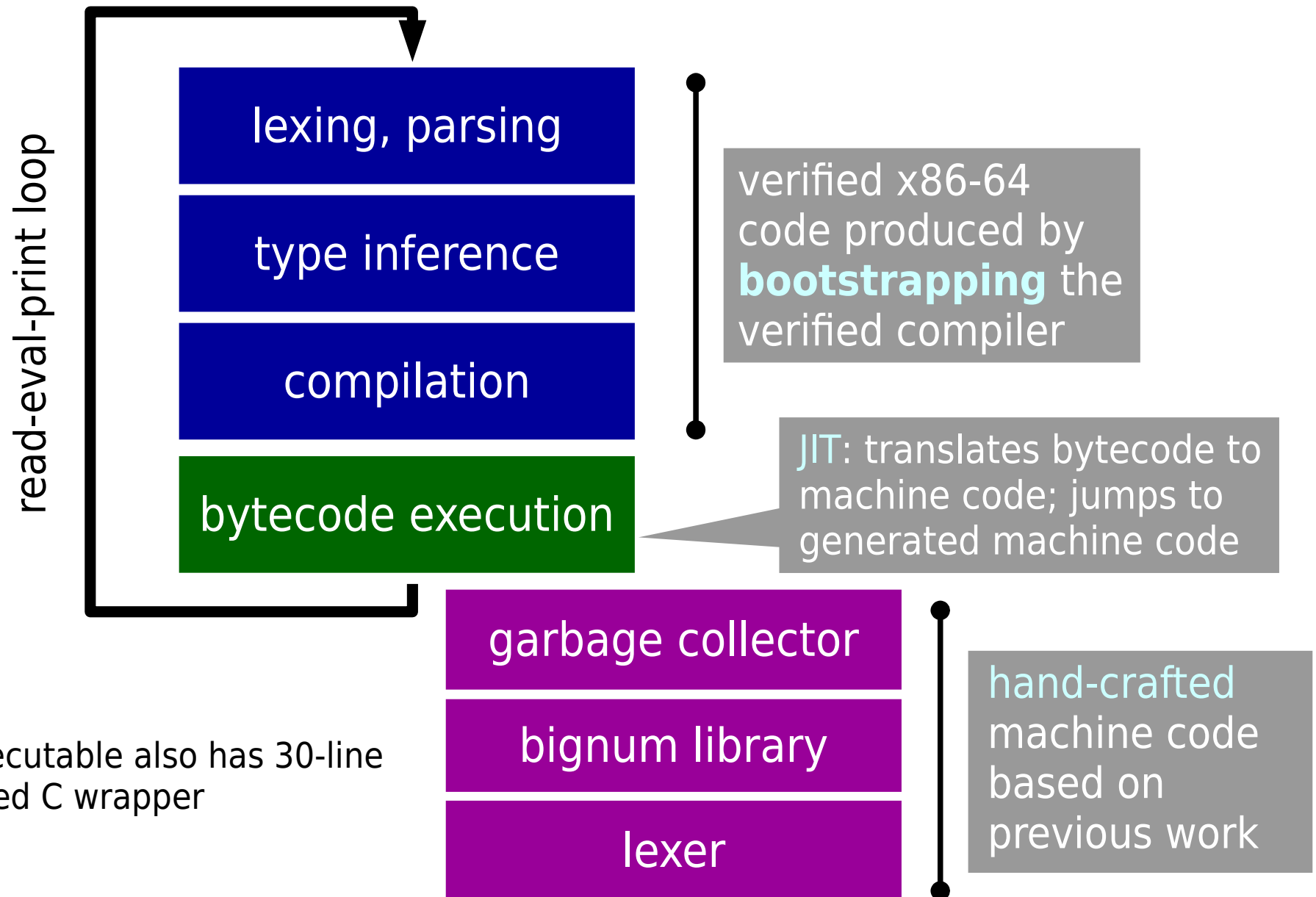
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JIT

# Bootstrapping

# Top-level Correctness Theorem

# Numbers and Summary

# Performance and Effort



<conclusion slide>  
<https://cakeml.org>